

Application No.: 10/640,349

Docket No.: JCLA11051

In The Claims:

Please amend the claims as follows:

Claim 1 (currently amended) A graphics display method for continuously displaying graphics data on multiple display devices of a computer that contains a system memory directly accessed by a CPU, the method comprising:

using a common clock source to synchronize blank periods of the display devices;

receiving a power saving signal from the CPU, said power saving signal indicates a request for executing a power saving process by the CPU during an non-responding period; and

executing the power saving process within the least common multiple occurrence of the blank periods of the display devices.

Claim 2 (original) The method of claim 1 further comprising a step of detecting the upcoming least common multiple occurrence of the blank periods of the display devices before the executing step.

Claim 3 (original) The method of claim 1 wherein the blank period can be a horizontal blank period (HBP) or a vertical blank period (VBP).

Claim 4 (original) The method of claim 3 wherein the horizontal blank period or the vertical blank period is provided by a graphics-processing unit.

Claim 5 (currently amended) A graphics display method for continuously displaying graphics data on multiple display devices of a computer that contains a system memory directly accessed by a CPU, the method comprising:

using a common clock source to synchronize blank periods of the display devices;

Application No.: 10/640,349

Docket No.: JCLA11051

receiving a power saving signal from the CPU, said power saving signal indicates a request for executing a power saving process by the CPU during an non-responding period;

detecting an upcoming overlapping blank period of the display devices that is long enough for the power saving process to take place; and

executing the power saving process within the overlapping blank period of the display devices.

Claim 6 (original) The method of claim 5 wherein the blank period can be a horizontal blank period (HBP) or a vertical blank period (VBP).

Claim 7 (original) The method of claim 6 wherein the horizontal blank period or the vertical blank period is provided by a graphics-processing unit.

Claim 8 (currently amended) A graphics display method for continuously displaying graphics data on multiple display devices of a computer system that contains a system memory directly accessed by a CPU, the method comprising:

receiving a power saving signal from the CPU, said power saving signal indicates a request for executing a power saving process by the CPU during an non-responding period;

detecting an occurrence of the upcoming blank period for each display device;

marking the last blank period occurrence of the display devices as a reference blank period;

extending the blank periods of the other display devices to a time where all the display devices have an overlapping blank period longer than a power saving process period; and

executing the power saving process within the overlapping blank period.

Application No.: 10/640,349**Docket No.: JCLA11051**

Claim 9 (original) The method of claim 8 wherein the blank period can be a horizontal blank period (HBP) or a vertical blank period (VBP).

Claim 10 (original) The method of claim 9 wherein the horizontal blank period or the vertical blank period is provided by a graphics-processing unit.

Claim 11 (original) The method of claim 8 further comprising a step of obtaining a length of the power saving process period from the CPU before the step of extending.

Claim 12 (currently amended) A graphics display method for continuously displaying graphics data on multiple display devices of a computer system that contains a system memory directly accessed by a CPU, the method comprising:

receiving a power saving signal from the CPU, said power saving signal indicates a request for executing a power saving process by the CPU during an non-responding period;

detecting an occurrence of the upcoming blank period for each display device;

marking the first blank period occurrence of the display devices as a reference blank period;

aligning the blank periods of the other display devices with the reference blank period so the display devices have an overlapping blank period longer than a power saving process period; and

executing the power saving process within the overlapping blank period.

Claim 13 (original) The method of claim 12 wherein the blank period can be a horizontal blank period (HBP) or a vertical blank period (VBP).

Application No.: 10/640,349

Docket No.: JCLA11051

Claim 14 (original) The method of claim 13 wherein the horizontal blank period or the vertical blank period is provided by a graphics-processing unit.

Claim 15 (original) The method of claim 12 further comprising a step of obtaining a length of the power saving process period from the CPU before the step of aligning.

Claim 16 (currently amended) The method of claim 12 wherein the step of aligning the blank periods of the other display devices with the reference blank period is to force the blank periods of the other display devices to occur at ~~around the time when~~ the reference blank period takes place.

Claim 17 (new) A graphics display method for continuously displaying graphics data on multiple display devices of a computer that contains a system memory directly accessed by a CPU, the method comprising:

using a common clock source to synchronize blank periods of the display devices;

receiving a power saving signal from the CPU, said power saving signal indicates a request for executing a power saving process by self-adjusting CPU frequency and a power level during an non-responding period; and

executing the power saving process within the least common multiple occurrence of the blank periods of the display devices.

Claim 18 (new) The method of claim 17, wherein while executing the power saving process, the system memory is continuously accessed by the CPU during the non-responding period.